

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q78657

Hien Boon TAN, et al.

Appln. No.: 10/581,395

Group Art Unit: Not Yet Assigned

Confirmation No.: 3868

Examiner: Not Yet Assigned

Filed: June 2, 2006

For: CHIP SCALE PACKAGE AND METHOD OF ASSEMBLING THE SAME

**PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of two months, extending the time for responding to the Notification of Missing Requirements of April 15, 2008 to August 15, 2008..

The statutory fee of \$460.00 is being paid via EFS filing screen. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880.

Respectfully submitted,

  
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Date: August 14, 2008